ABSTRACT

In a circuit driving a capacitive load Cp, current passed through a transistor Q3, a diode D1 and a recovering coil L is passed through lines L1, L2, and the inductance components of the lines L1 and L2, and the drain-source capacitances of the transistors Q1 and Q2 generate LC resonance. Capacitors C1 and C2 are connected in parallel to the drain-source regions of the transistors Q1 and Q2 to increase the total drain-source capacitance and reduce the resonance frequency, so that unwanted electromagnetic wave radiation in a frequency band affecting other electronic devices is suppressed.